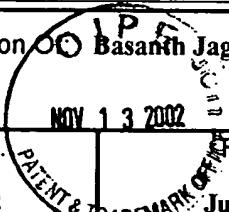


**SUBMISSION OF PROPOSED DRAWING AMENDMENT FOR APPROVAL BY
EXAMINER (37 CFR 1.121(a)(3)(ii) or 37 CFR 1.121(b)(3)(ii))**

FIS

In Re Application  Basanth Jagannathan, et al.

NOV 13 2002

Serial No.	Filing Date	Examiner
09/885,792	June 20, 2001	Christopher W. Lattin

Invention: A Non-Self-Aligned SiGe Heterojunction Bipolar Transistor"

Address to:

Assistant Commissioner for Patents
Washington, D.C. 20231

Attached please find:

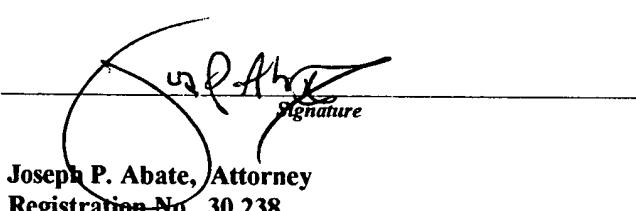
(check applicable items)

a sketch in permanent ink
 a copy of the original drawing(s) with red ink

BEST AVAILABLE COPY

showing proposed changes to the drawing(s) in this application for which the approval

Dated: November



Signature

Joseph P. Abate, Attorney
Registration No. 30,238
International Business Machines Corporation
Dept. 18G / Bldg. 300-482
2070 Route 52
Hopewell Junction, New York 12533
Phone: 845-894-4633 Fax: 845-892-6363

I certify that this document was
November 5, 2002 with the
class mail under 37 C.F.R. 1.8 and
Assistant Commissioner for Patents,
20231.



Signature of Person Mailing Correspondence

Karen Cinq-Mars
Typed or Printed Name of Person Mailing Correspondence